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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/632,988

08/04/2003

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YHK-0115

2974

34610 7590 08/17/2007  
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EXAMINER

BODDIE, WILLIAM

ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

08/17/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/632,988	<b>Applicant(s)</b> KANG ET AL.	
	<b>Examiner</b> William L. Boddie	<b>Art Unit</b> 2629	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1,4-7,9-11,13-15 and 20-22 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,4-7,9-11,13-15 and 20-22 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

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|--|--|
| <p>1) <input type="checkbox"/> Notice of References Cited (PTO-892)</p> <p>2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)</p> <p>3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br/>Paper No(s)/Mail Date <u>3/20/07, 7/18/07</u>.</p> | <p>4) <input type="checkbox"/> Interview Summary (PTO-413)<br/>Paper No(s)/Mail Date. ____.</p> <p>5) <input type="checkbox"/> Notice of Informal Patent Application</p> <p>6) <input type="checkbox"/> Other: ____.</p> |
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### DETAILED ACTION

1. In an amendment dated May 31<sup>st</sup>, 2007, the Applicant amended claim 1. Currently claims 1, 4-7, 9-11, 13-15 and 20-22 are currently pending.

### *Response to Arguments*

2. Applicant's arguments with respect to claims 1, 4-7, 9-11, 13-15 and 20-22 have been considered but are moot in view of the new ground(s) of rejection.

### *Claim Rejections - 35 USC § 103*

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1, 4-7, 9-10 and 20-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tokunaga (JP 2002-207449) in view of applicant's admitted prior art (figures 3 and 5, hereinafter referred to as APA).

**With respect to claim 1**, Tokunaga discloses, a method of driving a plasma display panel using frames, each frame divided into a plurality of sub-fields, comprising (fig. 2; for example):

applying a first driving waveform to said sub-fields at a temperature at a first prescribed temperature (top X and Y waveforms in fig. 8); and

applying a second driving waveform different from the first driving waveform to said sub-fields at a second prescribed temperature, the first and second prescribed temperature being different (bottom X and Y waveforms in fig. 8; para. 23),

wherein each of said sub-fields includes a plurality of periods (TR,TA,TS in fig. 5), one of the periods being an initialization period (TR in fig. 5), which is divided into a set-up interval for forming wall charges at a discharge cell (Pry1 in figs. 5 and 8) and a set-down interval for erasing a portion of the wall charges formed in the set-up interval (Pry2 in figs. 5 and 8; para. 16).

wherein each of the sub-fields includes a sustain period (TS in fig. 5) and wherein the sustain period and a number of sustain pulses in the sustain period of each sub-field is independent of a temperature of the plasma display panel (para. 23 of Tokunaga clearly discloses, only altering the pulse width of the initialization pulses, Pr1,2 and Prx. This does not alter the sustain period or the number of sustain pulses in the sustain period).

Tokunaga does not expressly disclose, that the set-up interval waveforms are different from each other, while waveforms applied in the other periods are substantially identical to each other.

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are admitted prior art).

Tokunaga and APA are analogous art because they are both from the same field of endeavor namely, driving waveforms for plasma displays.

At the time of the invention it would have been obvious to apply different waveforms in the set-up interval while applying substantially identical waveforms in

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other periods, as taught by APA, to the driving waveforms of Tokunaga. To further explain, Tokunaga discloses, applying different waveforms based on the panel temperature. Applicant admits prior art for two different set-up waveforms. One waveform, while improving contrast, causes brightness misfires at certain temperatures. It seems obvious that one of ordinary skill in the art at the time would have thought to replace the driving waveforms of Tokunaga with the different APA driving waveforms.

The motivation for doing so would have been to improve the contrast of the display (APA, para. 20) and to reduce brightness misfires (APA, para. 33).

**With respect to claim 4,** Tokunaga and APA disclose, the method as claimed in claim 1 (see above).

While Tokunaga does not expressly disclose, the steps of:

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval when said first driving waveform is supplied;

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell in the first half of the set-up interval; and floating the sustain electrode in the second half of the set-up interval.

APA discloses such a waveform in figure 5, with rising ramp (Ramp-up) and ground voltage and floating (Z set-up period).

It would have been obvious to one of ordinary skill in the art to replace the waveforms of Tokunaga with the driving waveform of APA for the benefit of improved contrast (APA, para. 20).

**With respect to claim 5**, Tokunaga and APA disclose, the method as claimed in claim 1 (see above).

While Tokunaga does not expressly disclose, the steps of:

Applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval when said second driving waveform is supplied and

Applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell.

APA discloses such a waveform in figure 3, with rising ramp (Ramp-up) and ground voltage (Z set-up period).

It would have been obvious to replace the waveforms of Tokunaga with the driving waveform of APA for the benefit of reduction of brightness misfires (APA, para. 33).

**With respect to claim 6**, Tokunaga and APA disclose, the method as claimed in claim 1 (see above).

Tokunaga does not expressly disclose, wherein said second prescribed temperature is within a range of temperature is 20° C. to -50° C.

While Tokunaga does not expressly disclose wherein said second prescribed temperature is within a range of temperature is 20° C. to -50° C, this further limitation is merely a design choice and would have been an obvious temperature range choice as this is approximately the range that a brightness misfire is likely to occur at when using conventional drive waveforms (APA, para. 33).

**With respect to claim 7**, Tokunaga discloses, a method of driving a plasma display panel using frames, each frame being divided into a plurality of subfields (fig. 4), an initialization period (TR in fig. 5) included in each sub-field is divided into a set-up interval (Pry1 in fig. 8) and a set-down interval for its driving (Pry2 in fig. 8), comprising the steps of displaying a picture on the panel (problem to be solved); sensing a driving temperature of the panel (90 in fig. 1); and setting a driving waveform to be applied in the set-up interval in correspondence with said driving temperature of the panel (para. 23),

wherein a first driving waveform (top X and Y waveforms in fig. 8) supplied when said driving temperature of the panel is a first prescribed temperature is different from a second driving waveform (bottom X and Y in fig. 8) supplied when said driving temperature of the panel is a second prescribed temperature, which is different from the first prescribed temperature (para. 23), and

wherein each of said sub-fields includes a plurality of periods (TR,TA,TS in fig. 5), one of the periods being an initialization period (TR in fig. 5).

Tokunaga does not expressly disclose, that specifically the set-up interval waveforms are different from each other, while waveforms applied in the other periods are substantially identical to each other.

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are prior art).

At the time of the invention it would have been obvious to apply different waveforms in the set-up interval while applying substantially identical waveforms in other periods, as taught by APA, to the driving waveforms of Tokunaga. To further explain, Tokunaga discloses, applying different waveforms based on the panel temperature. Applicant admits prior art for two different set-up waveforms. One waveform, while improving contrast, causes brightness misfires at certain temperatures. It seems obvious that one of ordinary skill in the art at the time would have thought to replace the driving waveforms of Tokunaga with the different APA driving waveforms.

The motivation for doing so would have been to improve the contrast of the display (APA, para. 20) and to reduce brightness misfires (APA, para. 33).

**With respect to claim 9**, Tokunaga and APA disclose, the method as claimed in claim 7 (see above), and altering the driving waveform in response to panel temperature.

Tokunaga does not expressly disclose the steps of:

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval; and

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell during the set-up period.

APA discloses such a waveform in figure 3, with rising ramp (Ramp-up) and ground voltage (Z set-up period).



It would have been obvious to replace the waveforms of Tokunaga with the driving waveform of APA for the benefit of reduction of brightness misfires (APA, para. 33).

**With respect to claim 10**, Tokunaga discloses, the method as claimed in claim 8 (see above), and altering the driving waveform in response to panel temperature.

Tokunaga does not expressly disclose the steps of:

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval; and

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell; and

floating the sustain electrode in the second half of the set-up interval.

APA discloses such a waveform in figure 5, with rising ramp (Ramp-up) and ground voltage and floating (Z set-up period).

It would have been obvious to replace the waveforms of Tokunaga with the driving waveform of APA for the benefit of improved contrast (APA, para. 20).

**With respect to claims 20-21**, the only additional limitation these claims present over their independent claims is that the first temperature is higher than the second temperature. Tokunaga clearly states that one temperature range is higher than a preset value and one range is below that same preset value (para. 23).

5. Claims 11, 13-15 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Awamoto et al. (US 6,720,940) in view of Nagai (US 6,011,355) and

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further in view of applicant's admitted prior art (figures 3 and 5, hereinafter referred to as APA).

**With respect to claim 11**, Awamoto discloses, a driving apparatus for a plasma display panel, comprising:

- a temperature sensor for sensing a driving temperature of the panel (75 in fig. 3);
- a controller (69, 61 in fig. 3) for controlling a turning-on and a turning-off of interval setting device (71 in fig. 3) during an initialization period of each sub-field of a frame, which includes a set-up interval (Prx in fig. 12) and a set-down interval (remainder of TR in fig. 12), in correspondence with a temperature inputted from the temperature sensor (71, 72 and 61 in fig. 3),

wherein said controller differently controls said turning-on and turning-off of the interval setting device (71 in fig. 3) when a driving temperature inputted from the temperature sensor is a first prescribed temperature and when a driving temperature inputted from the temperature sensor is a second prescribed temperature, the first and second temperatures being different (clear from operation of device, also see col. 7, lines 1-17).

Awamoto does not expressly disclose, a switching device provided between a plurality of common sustain electrodes provided at the panel and a ground voltage source, or that the set-up intervals are different from each other amongst the waveforms.

Nagai discloses, a switching device (28 in fig. 1), turned on and off by a controller (107A in fig. 4), provided between a plurality of common sustain electrodes (X in fig. 1) provided at the panel and a ground voltage source (clear from fig. 1).

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are prior art).

Nagai, APA and Awamoto are analogous art because they are all from the same field of endeavor namely, plasma displays driving methods.

At the time of the invention it would have been obvious to one of ordinary skill in the art to replace the interval setting device of Awamoto with the switching device, disclosed by Nagai.

The motivation for doing so would have been to hold the sustain electrodes at a ground level (Nagai, col. 12, lines 19-20).

At the time of the invention it would have been obvious to one of ordinary skill in the art to apply a different set-up interval while applying substantially identical waveforms in other periods, as taught in the waveforms of the APA, to the waveforms of Awamoto and Nagai.

The motivation for doing so would have been to reduce the chance of noise affecting the electrodes, and to improve the contrast (APA, para. 20).

**With respect to claim 13**, Tokunaga, APA and Nagai disclose, the driving apparatus as claimed in claim 11 (see above). They also disclose as shown above in

claim 11 limitations, using the timing controller of Tokunaga to control the switching device of Nagai.

Tokunaga and Nagai do not expressly disclose the times at which the switching device is turned on and off, thereby floating the common sustain electrode when a driving temperature inputted from the temperature sensor is more than said low temperature. However, the circuitry operation described in the current claim, would be required when the waveforms of APA are combined with the device of Awamoto and Nagai.

In other words, when the waveforms of the APA are used to drive the device of Awamoto and Nagai, the controller would turn on the switching device in a first half of the set-up interval while turning off the switching device in a second half of the set-up interval thereby floating the common sustain electrode when a driving temperature inputted from the temperature sensor is said first prescribed temperature.

As shown above APA discloses, such a waveform in figure 5, floating common sustain electrode (Z set-up period).

It would have been obvious to replace the more than low temperature waveform of Tokunaga with the driving waveform of APA and using the switching device of Nagai to implement the waveform for the benefit of improved contrast (APA, para. 20).

**With respect to claim 14**, Awamoto, APA and Nagai disclose, the driving apparatus as claimed in claim 11(see above).

While Awamoto and Nagai do not expressly disclose, wherein said controller turns on the switching device during the set-up interval when a driving temperature

inputted from the temperature sensor is said second prescribed temperature. However, the circuitry operation described in the current claim, would be required when the waveforms of APA are combined with the device of Tokunaga and Nagai.

In other words, when the waveforms of the APA are used to drive the device of Awamoto and Nagai, the controller would turn on the switching device in during the set-up interval when a driving temperature inputted from the temperature sensor is said second prescribed temperature.

As shown above, APA disclose, such a waveform in figure 3, with ground voltage (Z set-up period).

It would have been obvious to replace the second prescribed temperature waveform of Awamoto with the driving waveform of APA and using the switching device of Nagai to implement the waveform for the benefit of reduction of brightness misfires (APA, para. 33) and to hold the electrodes at ground when not driving the panel (Nagai, col. 12, lines 19-20) to obtain the invention as specified in claim 14.

**With respect to claim 15,** Awamoto, APA and Nagai disclose, the driving apparatus as claimed in claim 11 (see above).

Awamoto further discloses:

a sustain driver for driving the common sustain electrode (66 in fig. 3);

a scan driver for driving a plurality of scan electrodes provided in parallel with the common sustain electrode (67 in fig. 3); and

a data driver for driving a plurality of address electrode provided in a direction crossing the common sustain electrode (68 in fig. 3),

wherein said timing controller controls the sustain drive and the scan driver and the data driver (note fig. 3 and col. 6, lines 37-41).

**With respect to claim 22**, the only additional limitation this claim presents over its independent claim is that the first temperature is higher than the second temperature. Awamoto clearly states that one temperature range is higher than a preset value and one range is below that same preset value (col. 7, lines 3-8).

### ***Conclusion***

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to William L. Boddie whose telephone number is (571)


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272-0666. The examiner can normally be reached on Monday through Friday, 7:30 - 4:30 EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Wlb  
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